Institut FÜr Theoretische Informatik
Leibniz Universität Hannover

# Logical Charactarizations of algebraic circuit classes over rings 

Timon Barlag and Sabrina Alexandra Gaube

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(1) Introduction
(2) Models and Logics
(3) Some Characterizations

4 Outlook

## Introduction

- Parallel computation over an integral domain $R$
- Algebraic circuits (over $R$ )

$$
\text { - } A C_{R}^{0}
$$

- Logics \& Descriptive complexity
- $\mathrm{AC}^{0}=\mathrm{FO}$ [Im89]
- $\mathrm{NC}^{1}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {bound }}$ [DHV18]
- $\mathrm{AC}^{1}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}[\mathrm{DHV} 18]$
- $A C_{\mathbb{R}}^{0}=\mathrm{FO}_{\mathbb{R}}\left[\mathrm{Arb}_{\mathbb{R}}\right][\mathrm{BV} 21]$


## Basic algebraic definitions

## Recall:

A ring (with unity) is a set $R$ equipped with two binary operations + and $\times$, such that

- $(R,+)$ is an abelian group,
- $(R, \times)$ is a monoid,
- in particular, there is a $1 \in R$ such that $r \times 1=1 \times r=r$, for all $r \in R$.
- multiplication is distributive with respect to addition


## Basic algebraic definitions

## Recall:

An integral domain is a nonzero commutative ring without zero divisors, i.e. for every $a, b \neq 0 \in R: a \times b \neq 0$

- every field (e.g. $\mathbb{R}, \mathbb{Q}, \mathbb{C}, \mathbb{F}_{p}$, for a prime $p$ )
- $\mathbb{Z}, \mathbb{R}[x], \mathbb{Z}[i] \ldots$


## Algebraic Circuits over $R$

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- Directed Acyclic Graph with node types:
- input (fan-in 0)
- constant (fan-in 0 )
- arithmetic (fan-in $\geq 0$ )
- one of
- sign (fan-in 1)
- < (fan-in 2)
- output (fan-in 1)


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- size: number of gates
- depth: longest path from input to output
- non-uniform!



## Circuit families

## Circuit families

- Sequences $\mathcal{C}=\left(\mathcal{C}_{1}, \mathcal{C}_{2}, \ldots\right)$ of circuits where $\mathcal{C}_{i}$ has $i$ input gates
- If $\mathcal{C}_{i}$ computes $f_{\mathcal{C}_{i}}$ for all $i \in \mathbb{N}$, then $\mathcal{C}$ computes $f_{\mathcal{C}}(w)=f_{\mathcal{C}_{|w|}}(w)$.



## Circuit families

## Circuit families deciding sets

A circuit family $\mathcal{C}$ decides a set $S \subseteq \bigcup_{n \in \mathbb{N}} R^{n}$ iff $\mathcal{C}$ computes the characteristic function of $S$.

## Circuit classes

- $\mathrm{NC}_{R}^{i}$ : sets decided by bounded fan-in circuits families of size $\mathcal{O}\left(n^{\mathcal{O}(1)}\right)$ and depth in $\mathcal{O}\left(\log (n)^{i}\right)$
- $\mathrm{AC}_{R}^{i}$ : sets decided by unbounded fan-in circuit families of size $\mathcal{O}\left(n^{\mathcal{O}(1)}\right)$ and depth in $\mathcal{O}\left(\log (n)^{i}\right)$


## Uniformity

## Uniform circuit families

- There is an $R$-machine $M$ producing the circuit family.
- i.e. $M$ computes the function $n \mapsto \mathcal{C}_{n}$
- $M$ works in polynomial time $\rightarrow P$-uniform
- $\mathrm{U}_{\mathrm{P}}-C$ is the subclass of $C$ decided by $P$-uniform families
- $M$ works in logarithmic time $\rightarrow L$-uniform
- $\mathrm{U}_{\mathrm{LT}}-C$ is the subclass of $C$ decided by $L$-uniform families


## First-order Logic over $R$

$$
\varphi:=\exists x_{1} \exists x_{2} \operatorname{val}\left(x_{1}\right)=\operatorname{val}\left(x_{2}\right)+\pi \wedge x_{1}=\operatorname{succ}\left(x_{2}\right)
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## Definition: metafinite $R$-structures

- $R$-structure $\mathcal{D}=(\mathcal{A}, \mathcal{F})$ of signature $\sigma=\left(L_{s}, L_{f}\right)$
- $\mathcal{A}$ : finite structure of $L_{s}$ with universe $A$
- the skeleton of $\mathcal{D}$
- $\mathcal{F}$ : finite set of functions $X: A^{k} \rightarrow R$ interpreting symbols in $L_{f}$
- the arithmetic part of $\mathcal{D}$


## First-order Logic over $R$

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\begin{gathered}
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## Definition: $F O_{R}$

- formulas and terms over signature $\sigma=\left(L_{s}, L_{f}\right)$ for variables $x_{1}, x_{2}, \ldots$
- index terms: variables, functions $f \in L_{s}$
- number terms: ring elements, functions $g \in L_{f}, t_{1}+t_{2}, t_{1} \times t_{2}, \operatorname{sign}\left(t_{1}\right)$
- formulas
- atomic: $t_{1}=t_{2}, t_{1} \leq t_{2}$, predicates $P \in L_{s}$
- non-atomic: closure of atomic formulas under Boolean connectives and quantification $(\exists, \forall)$


## First-order Logic over $R$ - Example

$$
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$$

Structure $\mathcal{D}$ :

$$
A=\{v \mid v \in C\},
$$

val $(v)=$ the value of $v$ on inputs $e$ and $\frac{1}{2}$,
$\operatorname{succ}(v)=$ the successor gate of $v$

Circuit $C$ :


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## Extensions to $F O_{R}$

## Additional functions / relations

$-F O_{R}[S]$ for a set $S$ of functions and relations

## Additional constructions

- the sum, product and maximization rules for creating number terms
- to use $\sum_{i \in A} t(i), \prod_{i \in A} t(i)$ and $\max _{i \in A}(t(i))$ in formulas

$$
\begin{gathered}
\varphi=\exists v_{1} \operatorname{sum}_{v_{2}}\left(g\left(v_{2}\right)\right)>g\left(v_{1}\right) \times 2 \\
A=\{\diamond, \boldsymbol{\uparrow}\}, g=\{\diamond \mapsto \pi, \boldsymbol{\oplus} \mapsto 42\}
\end{gathered}
$$

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$$
\begin{gathered}
\varphi=\exists v_{1} \overbrace{\operatorname{sum}_{v_{2}}\left(g\left(v_{2}\right)\right)}^{42+\pi}>g\left(v_{1}\right) \times 2 \\
A=\{\diamond, \boldsymbol{\uparrow}\}, g=\{\diamond \mapsto \pi, \boldsymbol{\phi} \mapsto 42\}
\end{gathered}
$$

## Logical Characterizations

Non-uniform $\mathrm{AC}_{R}^{0}$

$$
\mathrm{AC}_{R}^{0}=\mathrm{FO}_{R}\left[\mathrm{Arb}_{R}\right]
$$

Polynomial-time uniform $\mathrm{AC}_{R}^{0}$

$$
\mathrm{U}_{\mathrm{P}}-\mathrm{AC}_{R}^{0}=\mathrm{FO}_{R}\left[\mathrm{FTIME}_{R}\left(n^{\mathcal{O}(1)}\right)\right]
$$

Logarithmic-time uniform $\mathrm{AC}_{R}^{0}$

$$
\mathrm{U}_{\mathrm{LT}}-\mathrm{AC}_{R}^{0}=\mathrm{FO}_{R}\left[\mathrm{FTIME}_{R}(\log (n))\right]+\mathrm{SUM}_{R}+\mathrm{PROD}_{R}
$$

## Guarded predicative/functional recursion

## Definition: [Durand, Haak, Vollmer, 2018]

A formula $\varphi$ is in $\mathrm{FO}+\mathrm{GPR}^{1}$ if it has the form

$$
\varphi::=[P(\bar{x}, \bar{y}) \equiv \theta(\bar{x}, \bar{y}, P)] \varphi(P) \mid \psi
$$

where $\psi$ and $\theta$ are FO formulae with free variables $\bar{x}, \bar{y}$ such that each atomic sub-formula involving the symbol $P$
(1) is of the form $P(\bar{x}, \bar{z})$, where $\bar{z}$ is in the scope of a guarded quantification $Q \bar{z} .((\bar{z} \leq \bar{y} / 2) \wedge \xi(\bar{y}, \bar{z}))$ with $Q \in\{\forall, \exists\}, \xi \in \mathrm{FO}$ and
(2) never occur in the scope of any quantification not guarded this way.

## Results

## Former results [DHV, 2018]

(1) $\mathrm{NC}^{1}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {bound }}^{1}$
(2) $\mathrm{SAC}^{1}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {semi }}^{1}$

- $\mathrm{AC}^{1}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}^{1}$
- $\# \mathrm{AC}^{0}=\#$ Win- $\mathrm{FO}[\mathrm{BIT}]$
- $\# \mathrm{NC}^{1}=\#$ Win-FO[BIT] $+\mathrm{GPR}_{\text {bound }}^{1}$
- $\# \mathrm{SAC}^{1}=\#$ Win-FO[BIT $]+\mathrm{GPR}_{\text {semi }}^{1}$
- $\# \mathrm{AC}^{1}=\# \mathrm{Win}-\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}^{1}$


## Guarded predicative logic - ideas of adaptations

## ideas

- Extend GPR in order to characterize the whole AC, NC and SAC -hierachies
- Substitute the factor $\frac{1}{2}$ by $2^{\log _{2}(n) / \log _{2^{i}}(n)}$ for $\mathrm{AC}^{i}$ etc.
- But exponantiation is not part of the logic
$\Rightarrow \mathrm{GPR}_{R}^{i}$ by tuples
- Add a similar construction to logics over metafinite structures
- probably functional instead of predicative recursion


## Further results

## Goal

(1) $\mathrm{NC}^{i}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {bound }}^{i}$
(2) $\mathrm{SAC}^{i}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {semi }}^{i}$
(3) $\mathrm{AC}^{i}=\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}^{i}$
(9) $\# \mathrm{NC}^{i}=\# \mathrm{Win}-\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {bound }}^{i}$
(5) $\# \mathrm{SAC}^{i}=\# \mathrm{Win}-\mathrm{FO}[\mathrm{BIT}]+\mathrm{GPR}_{\text {semi }}^{i}$
(0) $\# \mathrm{AC}^{i}=\#$ Win-FO[BIT] $+\mathrm{GPR}^{i}$
(0) $\mathrm{NC}_{R}^{i}=\mathrm{FO}_{R}\left[\mathrm{Arb}_{R}\right]+\mathrm{GPR}_{R, \text { bound }}^{i}$
(8) $\mathrm{AC}_{R}^{i}=\mathrm{FO}_{R}\left[\mathrm{Arb}_{R}\right]+\mathrm{GPR}_{R}^{i}$

## Relationship between versions of $\mathrm{AC}_{R}^{0}$ over different rings - canonical maps

## Definition:

A canonical ring map from a ring $R_{1}$ to a ring $R_{2}$ is a fixed injective function $f_{R_{1}, R_{2}}: R_{1} \rightarrow R_{2}^{k}$ for some $k \in \mathbb{N}$.
$f_{R_{1}, R_{1}}$ is the identity function.

## Definition:

For two complexity classes $\mathcal{C}_{R_{1}}, \mathcal{C}_{R_{2}}$ over the respective rings $R_{1}, R_{2}$, we write

$$
\mathcal{C}_{R_{1}} \subseteq_{c} \mathcal{C}_{R_{2}}
$$

if for all languages $A \in \mathcal{C}_{R_{1}}$ the following holds:

$$
\left\{f_{R_{1}, R_{2}}(x) \mid x \in A\right\} \in \mathcal{C}_{R_{2}^{*}} .
$$

The relations $=_{c}$ and $C_{c}$ are defined analogously.

## special cases of $\mathrm{AC}_{R}^{0}$

## Theorem

$-\mathrm{AC}_{\mathbb{Q}}^{0}={ }_{c} \mathrm{AC}_{\mathbb{Z}}^{0}={ }_{c} \mathrm{AC}_{\mathbb{Z}[i]}^{0}={ }_{c} \mathrm{AC}_{\mathbb{Z}[x]}^{0}={ }_{c} \mathrm{AC}_{\mathbb{Z}[x, y]}^{0}$
$-\mathrm{AC}_{\mathbb{C}}^{0}={ }_{c} \mathrm{AC}_{\mathbb{R}[i]}^{0}={ }_{c} \mathrm{AC}_{\mathbb{R}}^{0}={ }_{c} \mathrm{AC}_{\mathbb{R}[x]}^{0}={ }_{c} \mathrm{AC}_{\mathbb{R}[x, y]}^{0}$

- $\mathrm{AC}_{\mathbb{Z}}^{0} \subset_{c} \mathrm{AC}_{\mathbb{R}}^{0}$


## idea:

Write a number $z=\frac{a}{b} \in \mathbb{Q}$ (resp. $\left.z=a+b i \in \mathbb{C}\right)$ as tuple $(a, b)$ and use a node per tuple.

## Future/Current Research

- How can these classes be contextualized?
- separate $\mathrm{AC}_{R}^{0}$ and $\mathrm{NC}_{R}^{1}$ ?
- Can we find a meaningful analogue of $\mathrm{TC}_{R}$ ?
- investigate then in particular the question, whether $\mathrm{TC}_{R}^{0}=\mathrm{NC}_{R}^{1}$ ?
- How are $\mathrm{AC}_{\mathbb{F}_{p}}^{0}$ and $\mathrm{AC}_{\mathbb{F}_{q}}^{0}$ for $p \neq q$ (prime?) related?
- Define $\mathrm{SAC}_{R}^{i}$ classes
- Is it important which gate type we bound?
- Is there a connection between GPR-variations and fixed point logics like $\mathrm{FO}(\mathrm{LFP})$ ?


## Sources

[Im89] Neil Immerman. Expressibility and Parallel Complexity. SIAM J. Comput. 18(3), 625-638 (1989)
[CM99] Felipe Cucker and Klaus Meer. Logics which capture complexity classes over the reals. J. Symb. Log., 64(1):363-390, 1999.
[BCSS98] Lenore Blum, Felipe Cucker, Michael Shub, and Steve Smale. Complexity and Real Computation. Springer-Verlag, New York, 1998.
[DHV18] Arnaud Durand, Anselm Haak, and Heribert Vollmer. Model-Theoretic Characterization of Boolean and Arithmetic Circuit Classes of Small Depth. LICS 2018: 354-363.
[BV21] Timon Barlag and Heribert Vollmer. A Logical Characterization of Constant-Depth Circuits over the Reals. WoLLIC 2021: 16-30

## Definition $\left(\mathrm{GPR}_{R}^{i}\right)$

For $i \geq 0$, a formula $\varphi$ is in $\mathrm{FO}_{R}\left(\operatorname{GPR}_{R}^{i}\right)$ if it has the form

$$
\left.\varphi::=\left[P\left(\bar{x}, \overline{y_{1}}, \ldots, \overline{y_{i}}\right) \equiv \theta\left(\bar{x}, \overline{y_{1}}, \ldots, \overline{y_{i}}, P\right)\right] \varphi(P)\right] \mid \psi,
$$

where $\psi$ and $\theta$ are $\mathrm{FO}_{R}$ formulae with free variables $\bar{x}, \overline{y_{1}}, \ldots, \overline{y_{i}}$ such that each atomic sub-formula involving the symbol $P$
(1) is of the form $P\left(\bar{x}, \overline{y_{1}}, \ldots, \overline{y_{i}}\right)$, the $\overline{y_{1}}, \ldots, \overline{y_{i}}$ are in the scope of a guarded aggregation

$$
A_{\overline{y_{1}}, \ldots, \overline{y_{i}}} \cdot\left(\bigvee_{j=i}^{1} \overline{z_{j}} \leq \overline{y_{j}} / 2 \wedge \bigwedge_{k=j-1}^{1} \overline{z_{k}} \leq \overline{y_{k}} \wedge \xi\left(\overline{y_{1}}, \ldots, \overline{y_{i}}, \overline{z_{1}}, \ldots, \overline{z_{i}}\right)\right)
$$

with $A \in\{$ max, sum, prod $\}$ and
(2) never occur in the scope of any aggregation (or quantification) not guarded this way.

